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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

TRAN, TRANG U

ART UNIT PAPER NUMBER

2614

DATE MAILED: 03/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/848,376

Applicant(s)

TUNG, CHUNG-CHIH

Examiner

Trang U. Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 29 December 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-17 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Response to Arguments***

1. Applicant's arguments with respect to claims 1-17 have been considered but are moot in view of the new ground(s) of rejection.

***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Iwaki (US Patent No. 6,567,097 B1) in view of Hong (US Patent No. 5,943,064).

In considering claim 1, Iwaki discloses all the claimed subject matter, note 1) the claimed obtaining a power on signal is met by the power supply of the personal computer (Fig. 7), 2) the claimed obtaining a channel selection signal when the power on signal is a TV selection signal is met by the satellite tuner 13 (Fig. 7, col. 6, lines 44-65), 3) the claimed obtaining a video signal according to the channel selection signal is met by satellite tuner 13 (Fig. 7, col. 6, lines 44-65), 4) the claimed capturing the video signal is met by the DVD decoder 15 (Fig. 7, col. 6, line 44 to col. 7, line 49), and 5) the claimed driving a display to turn the video signal into a visible image is met by the LCD display (Fig. 7, col. 7, line 50 to col. 8, line 21).

However, Iwaki explicitly does not disclose the newly added claimed obtaining a power on signal by a BIOS (Basic Input/Output System).

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Hong teaches that placing the apparatus in the first mode or the second mode may be accomplished during system initialization, during bootup, when the control registers are written to, in some embodiments, the graphics controller card may support Basic Input Output System (BIOS) interrupt functions for writing to the control registers as show at 45 in Fig. 2 (col. 8, lines 18-43).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the Basic Input Output System (BIOS) as taught by Hong into Iwaki' system in order to process multiple types of graphics pixel data for display in multiple different environments using a minimum number of components and it consumes less power than conventional systems (col. 3, lines 15-20) of Hong.

In considering claim 2, the claimed further comprising the step of loading in an OS when the power on signal is an operation selection signal is met by the CPU 11 which controls the operation of the entire system and executes an operating system, and application program to be executed, and the like, stored in the main memory 12 (Fig. 7, col. 6, lines 37-43) of Iwaki.

In considering claim 3, the claimed further comprising the step of initializing a video control unit, a video tuner unit, and a video capture unit is met by the computer system (Fig. 7), the video control unit, the video tuner unit and the video capture unit are initialized when the power turn on of Iwaki and the Basic Input Output System (BIOS) 45 in Fig. 2 (col. 8, lines 18-43) of Hong.

In considering claim 4, the claimed wherein the video control unit is a VGA chip is met by the VGA controller 100 (Fig. 1, col. 3, lines 25-39) of Iwaki.

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In considering claim 5, the combination of Iwaki and Hong discloses all the limitations of the instant invention as discussed in claim 1 above, except for providing the claimed wherein the video tuner unit is a video tuner chip. Using video tuner chip is old and well known in the art. Therefore, the Official Notice is taken. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the old and well known video tuner chip into the combination of Iwaki and Hong's system in order to reduce the size of the system because chips has smaller size.

In considering claim 6, the combination of Iwaki and Hong discloses all the limitations of the instant invention as discussed in claim 1 above, except for providing the claimed wherein the video capture unit is a video capture chip. Using video capture chip is old and well known in the art. Therefore, the Official Notice is taken. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the old and well known video capture chip into the combination of Iwaki and Hong's system in order to reduce the size of the system because chips has smaller size.

In considering claim 7, the claimed further comprising the step of initializing an audio control unit is met by the computer system (Fig. 7), the audio control unit is initialized when the power turn on of Iwaki and the Basic Input Output System (BIOS) 45 in Fig. 2 (col. 8, lines 18-43) of Hong.

In considering claim 8, the combination of Iwaki and Hong discloses all the limitations of the instant invention as discussed in claim 1 above, except for providing the claimed wherein the audio control unit is an audio chip. Using audio chip is old and

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well known in the art. Therefore, the Official Notice is taken. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the old and well known audio chip into the combination of Iwaki and Hong's system in order to reduce the size of the system because chips has smaller size.

In considering claim 9, the claimed wherein the step of capturing the video signal further comprises the steps of: demodulating the video signal into a tuned signal using the video tuner unit; and capturing the tuned signal as a capture signal using the video capture unit is met by the DVD decoder 15 (Fig. 7, col. 6, line 44 to col. 7, line 49) of Iwaki.

In considering claim 10, the claimed wherein the step of driving a display to turn the video signal into a visible image further comprises the steps of turning the capture signal into a visible image using the video control unit is met by the VGA controller 100 which controls and LCD display (Fig. 7, col. 7, line 50 to col. 8, line 21) of Iwaki.

In considering claim 11, the claimed further comprising the step of initializing a ZV port between the video capture unit and the video control unit is met by the ZV port (col. 3, lines 25-40 and col. 7, lines 16-24) of Iwaki.

In considering claim 12, the claimed wherein the computer is a notebook computer is met by Fig. 7, col. 6, lines 30-36 of Iwaki.

Claim 13 is rejected for the same reason as discussed in claims 1, 3, 4, 9 and 10.

Claim 14 is rejected for the same reason as discussed in claim 2.

Claim 15 is rejected for the same reason as discussed in claim 7.

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In considering claim 16, the claimed wherein the step of capturing the video signal comprising the step of: initializing a ZV port between the video capture chip and the VGA chip, and transmitting the capture signal to the VGA chip through the ZV port is met by the ZV port (col. 3, lines 25-40 and col. 7, lines 16-24) of Iwaki.

Claim 17 is rejected for the same reason as discussed in claim 12.

### ***Conclusion***

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Trang U. Tran** whose telephone number is **(703) 305-0090**.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **John W. Miller**, can be reached at **(703) 305-4795**.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

**(703) 872-9314 (for Technology Center 2600 only)**

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

TT TT  
March 19, 2004

  
**MICHAEL H. LEE**  
**PRIMARY EXAMINER**